

Notice of References Cited	Application/Control No. 10/631,062	Applicant(s)/Patent Under Reexamination HARRINGTON ET AL.	
	Examiner Richard Pantoliano Jr	Art Unit 2194	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-4,768,149	08-1988	Konopik et al.	710/47
*	B	US-5,065,354	11-1991	Jons et al.	714/5
*	C	US-5,446,877	08-1995	Liu et al.	714/1
*	D	US-5,455,919	10-1995	Brewer et al.	719/321
*	E	US-5,586,301	12-1996	Fisherman et al.	711/152
*	F	US-5,794,063	08-1998	Favor, John G.	712/23
*	G	US-5,909,567	06-1999	Novak et al.	712/208
*	H	US-6,397,382	05-2002	Dawson, Peter S.	717/130
*	I	US-7,085,705	08-2006	Traut, Eric P.	703/23
*	J	US-7,133,951	11-2006	Bourekas, Philip A.	710/260
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	"Itanium™ Processor Floating-point Software Assistance and Floating-point Exception Handling." Intel Corp., January 2000
*	V	Jones, Steve. "Using spinlocks in a symmetric multiprocessing environment." Tech Specialist, v2, n10, pg 15(6), Oct 1991
*	W	IBM Technical Bulletin: NNRD447149. "Method to Prevent Multiple Processes After Taking Exceptions To Enter Open Firmware In a Symmetrical Multiprocessor machine", Published 01 July 2001
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.